

# Digital Remote Control Transmitter

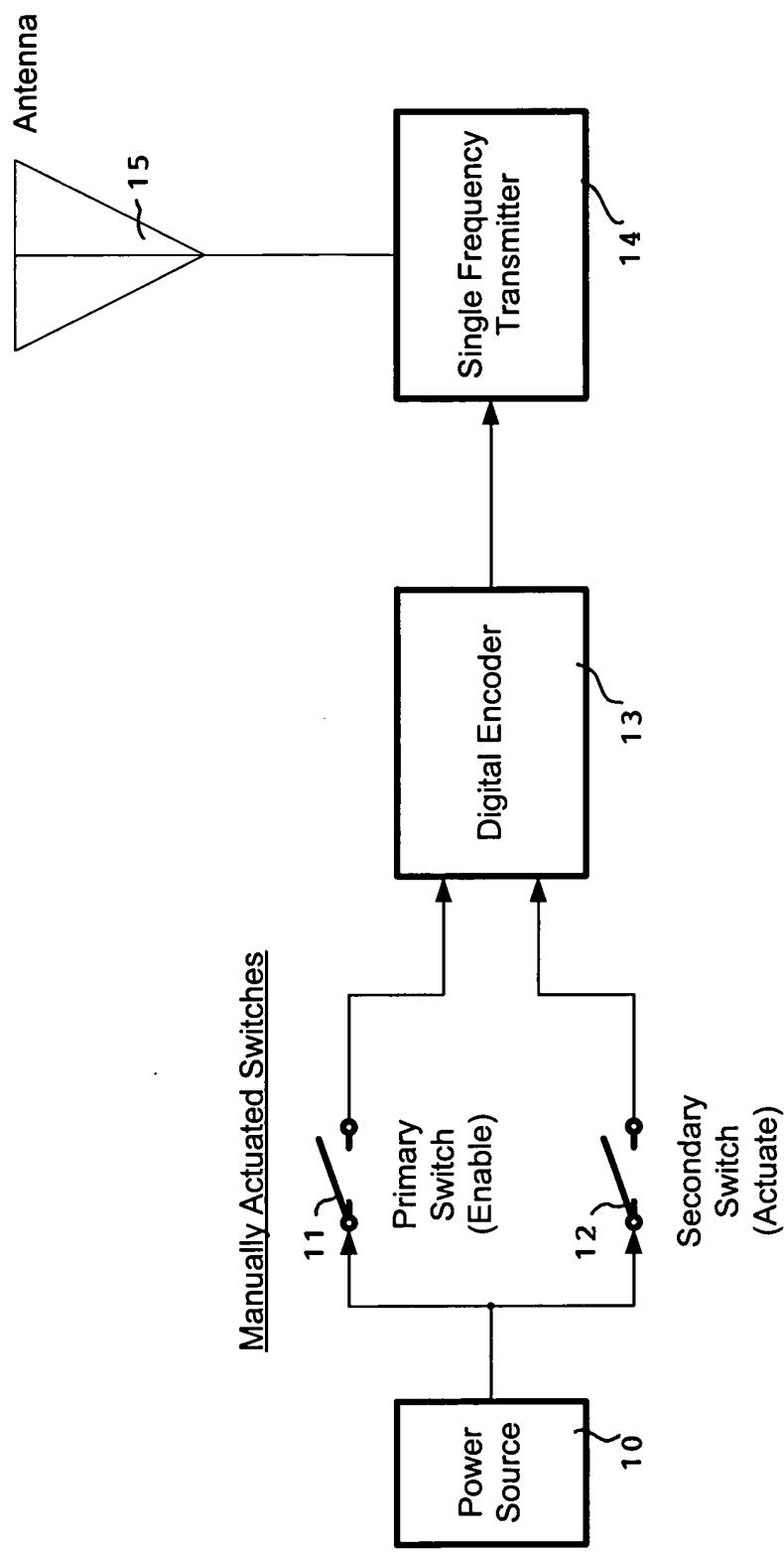


Figure 1

# Digital Remote Control Receiver

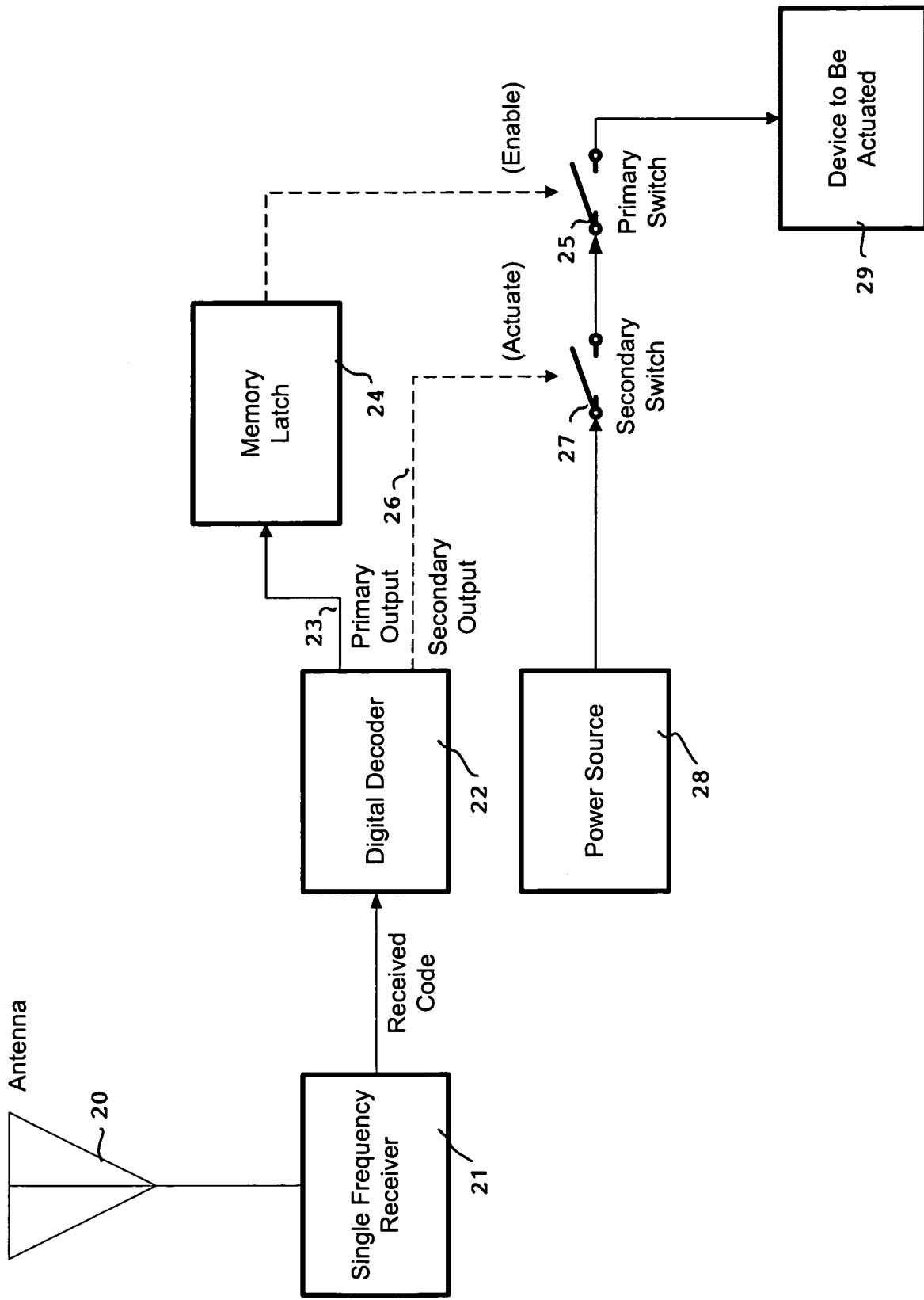


Figure 2

## Code Timing Diagram -- Preferred Embodiment

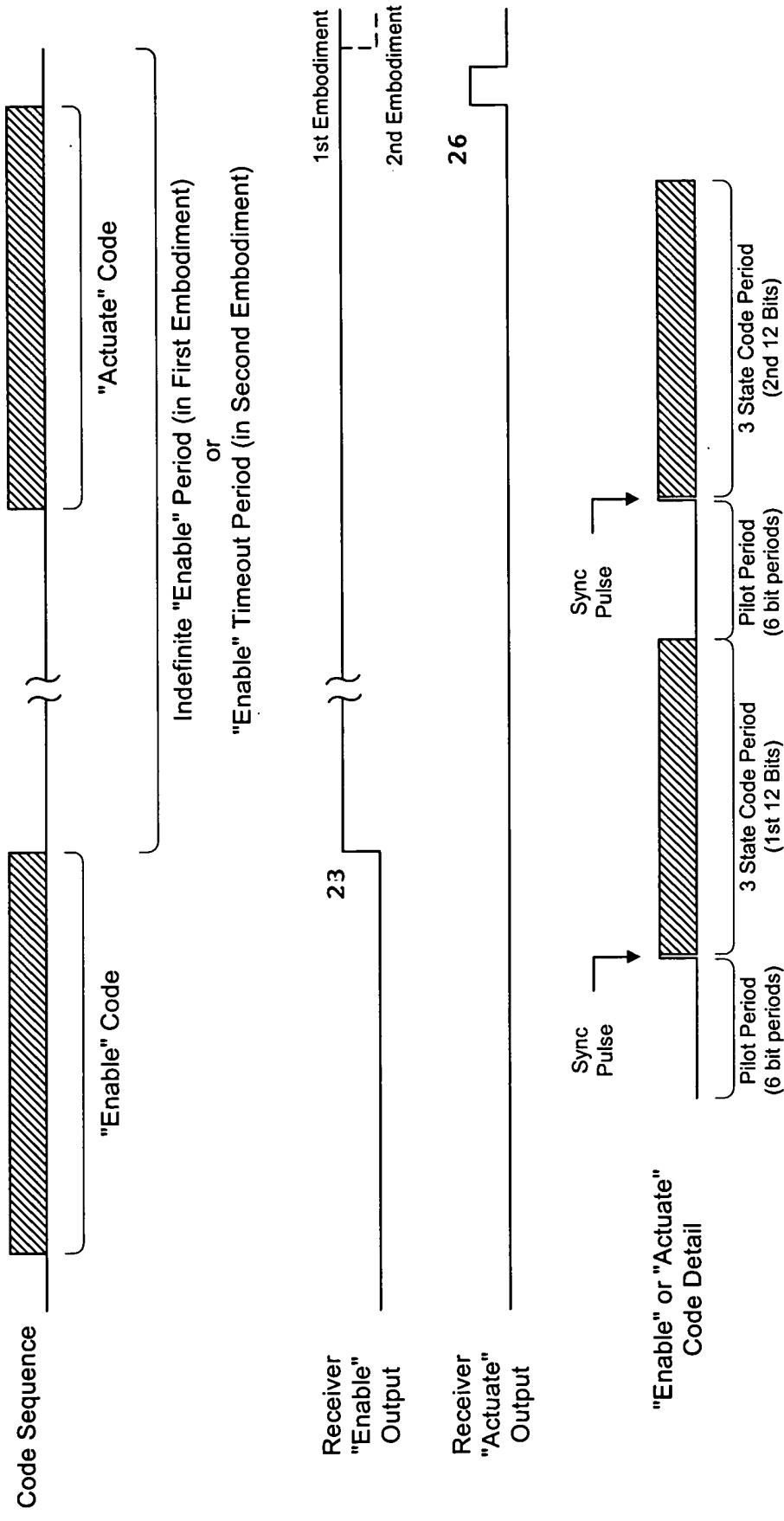
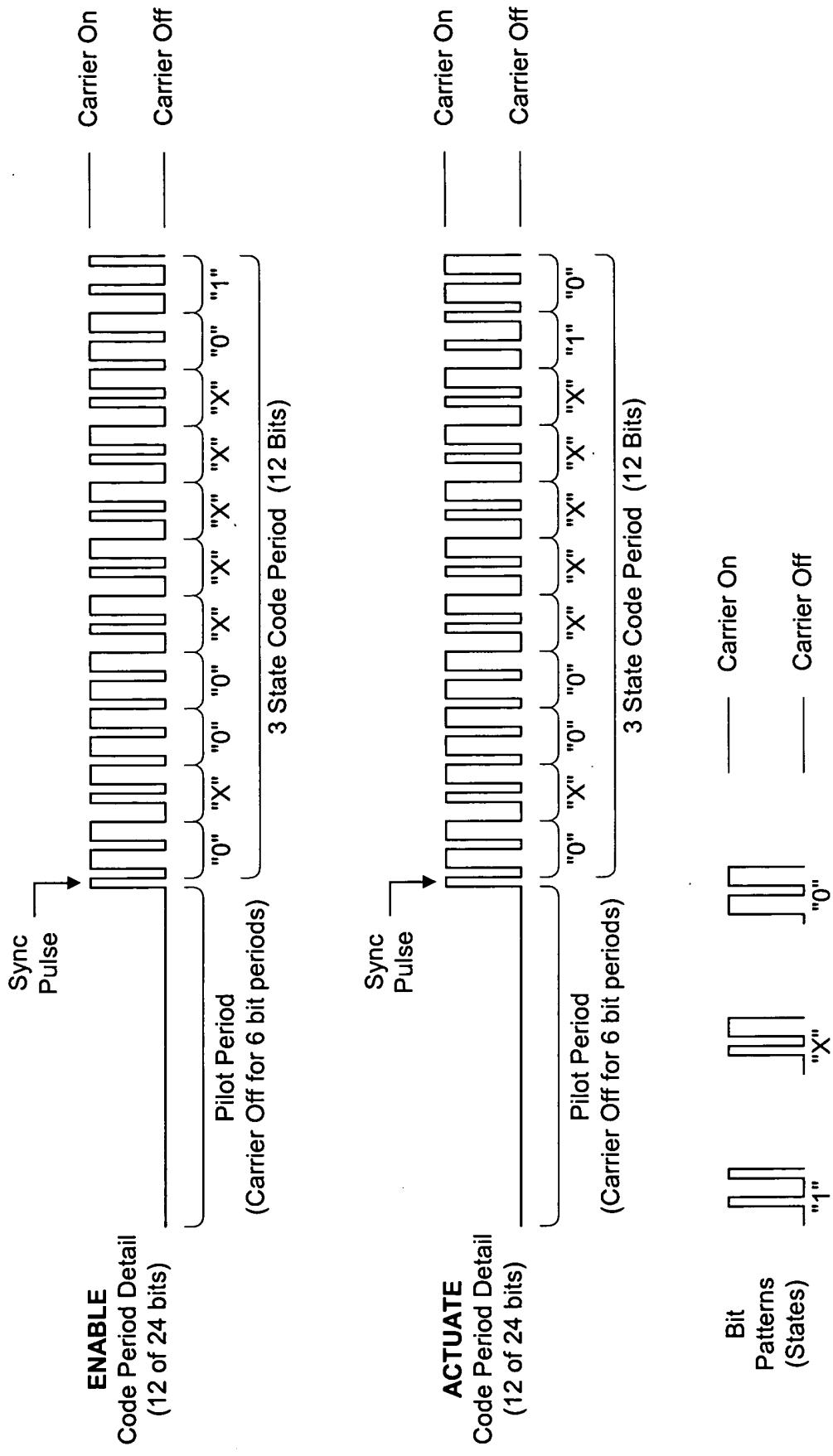


Figure 3

## Code Period Detail -- Preferred Embodiment



**Figure 4**

## Receiver Detailed Block Diagram -- Indefinite Enable Period

(In another embodiment the HT6032 and CD4013BE functions could be performed in a microcomputer chip, such as a Microchip PIC16C622A with embedded program.)

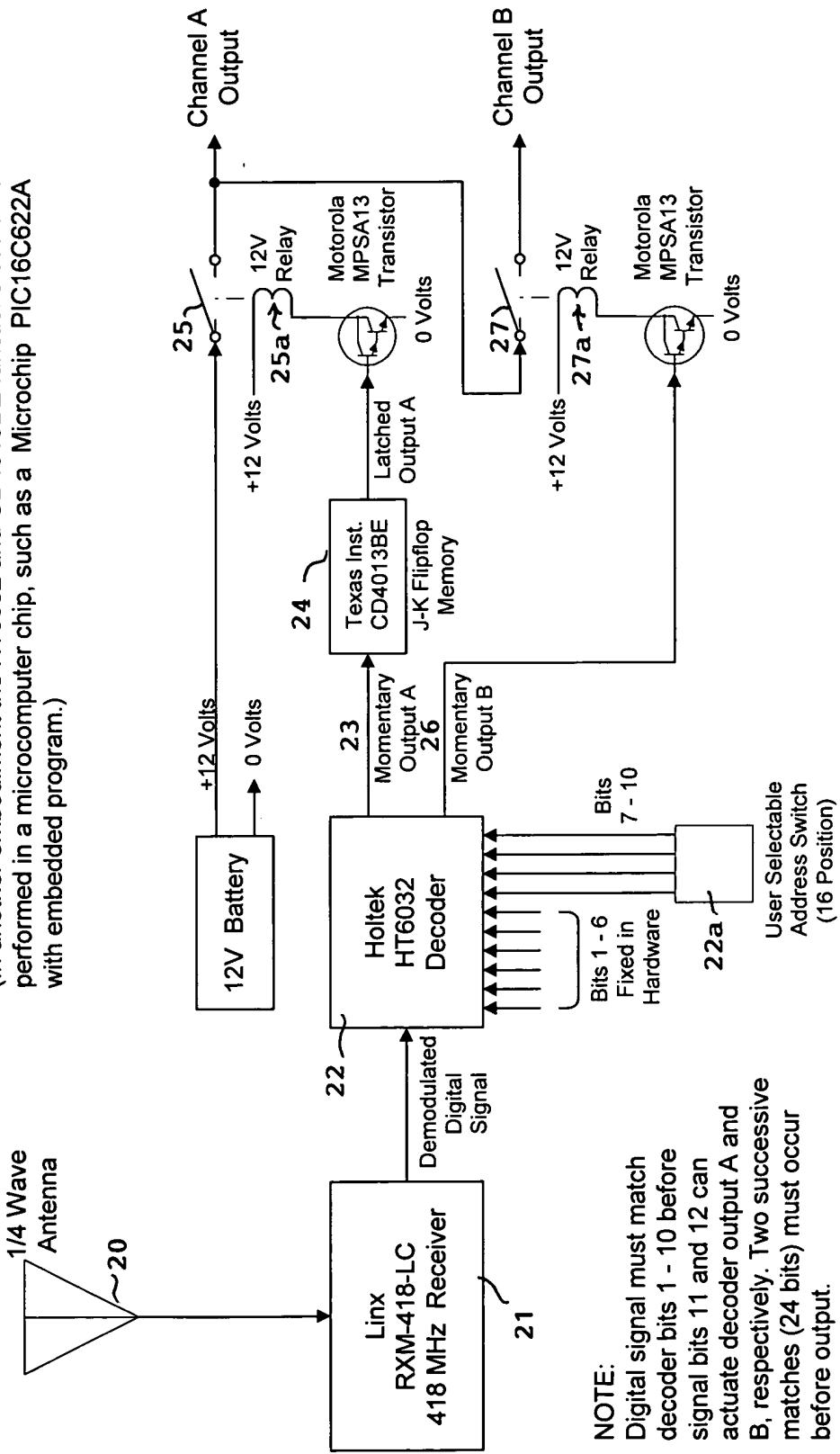


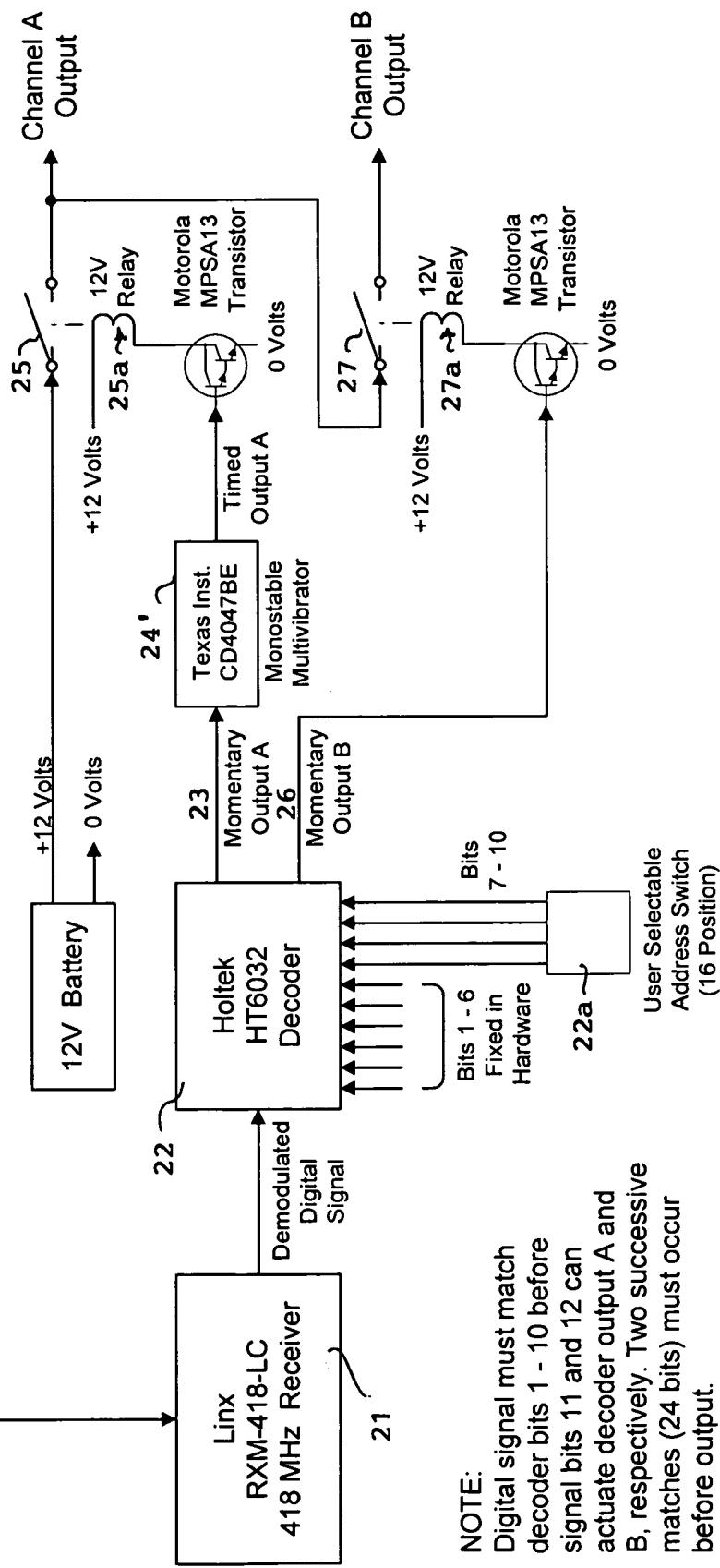
Figure 5

## Receiver Detailed Block Diagram -- Timed Enable Period

(In another embodiment the HT6032 and CD4047BE functions could be performed in a microcomputer chip, such as a Microchip PIC16C622A with embedded program.)

1/4 Wave  
Antenna

20



NOTE:  
Digital signal must match  
decoder bits 1 - 10 before  
signal bits 11 and 12 can  
actuate decoder output A and  
B, respectively. Two successive  
matches (24 bits) must occur  
before output.

Figure 6

## Transmitter Detailed Block Diagram

(In another embodiment the HT6012 encoding function could be performed in a microcomputer chip, such as a Microchip PIC16C622A with embedded program.)

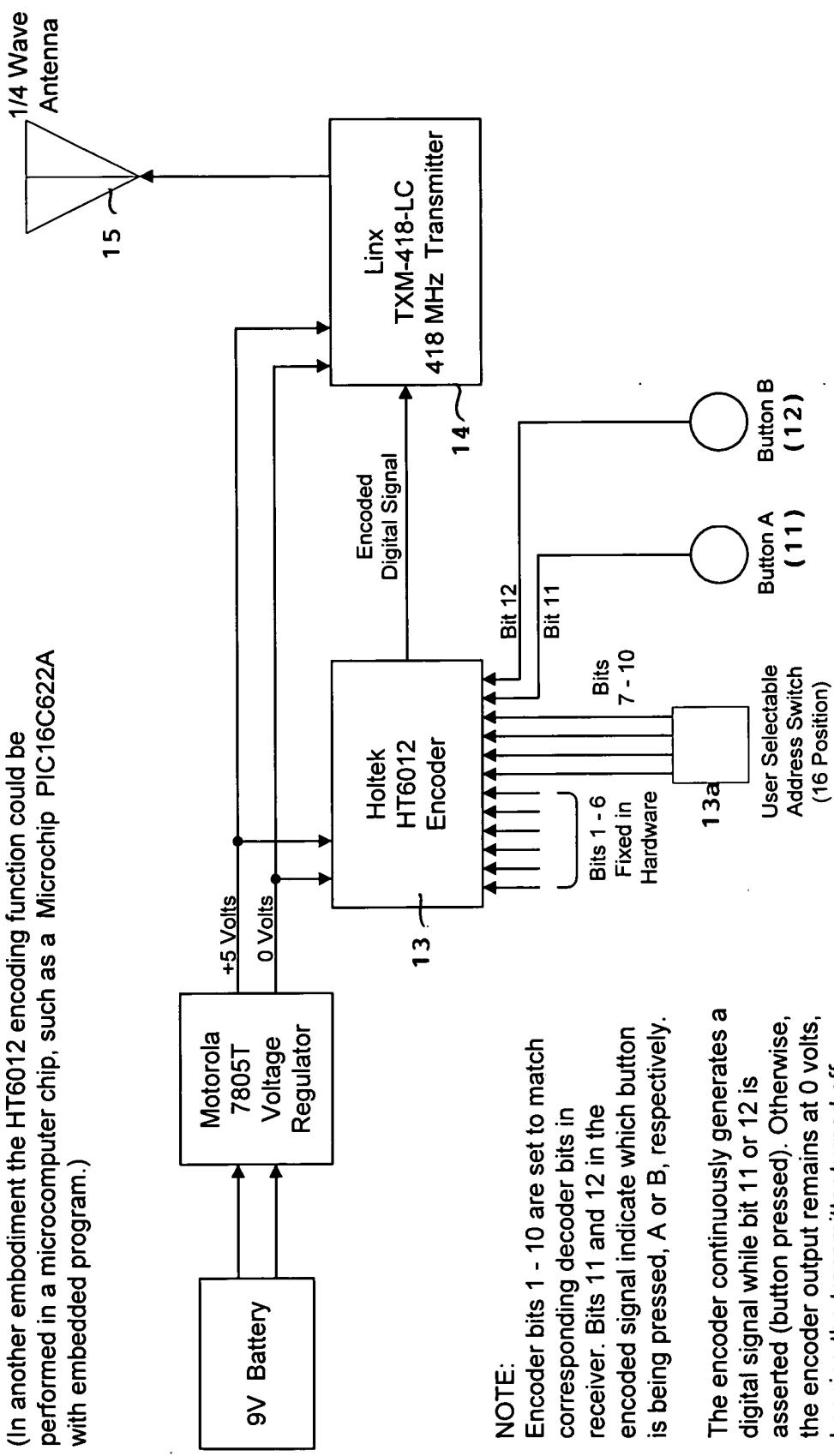
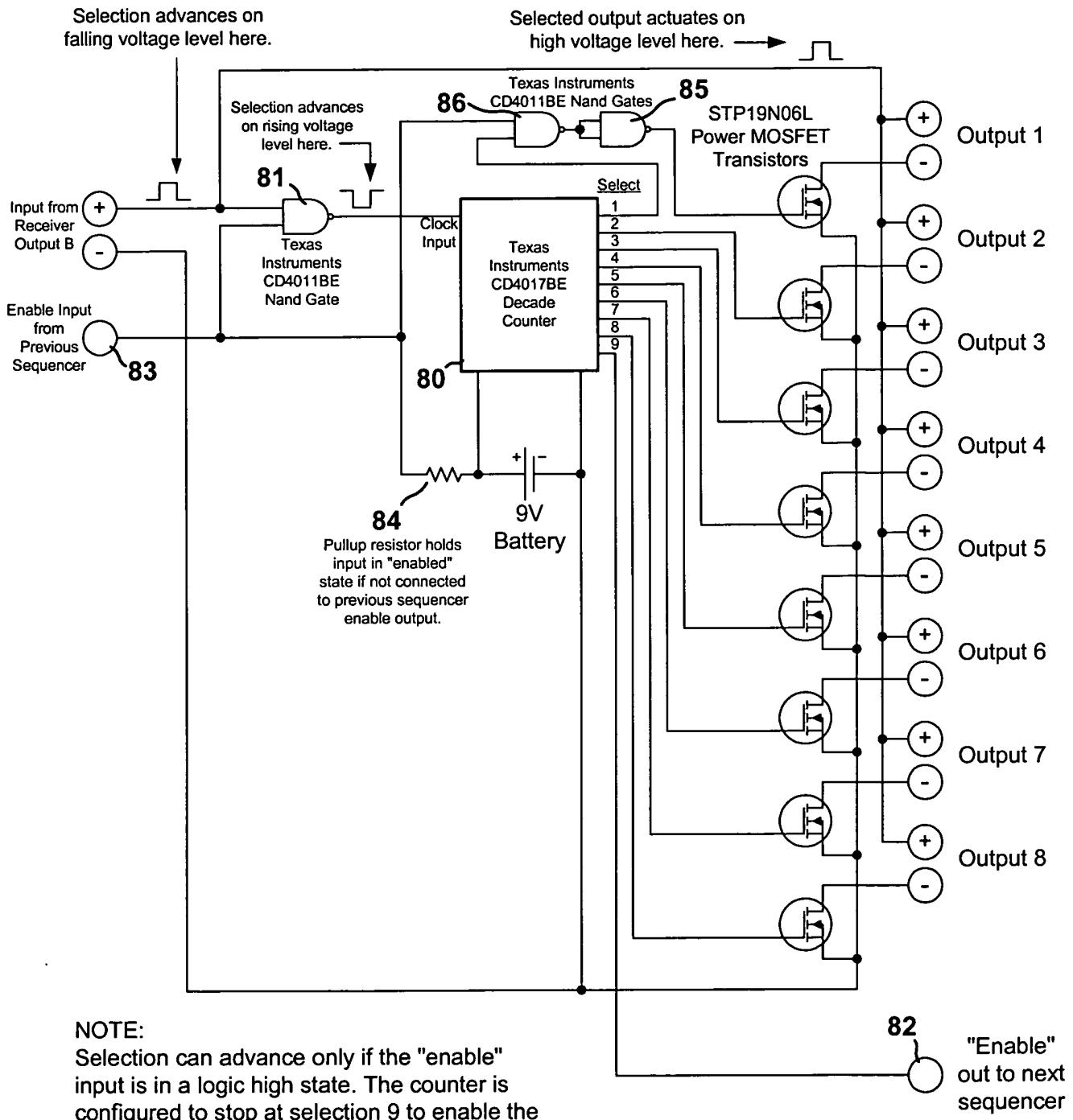


Figure 7

## Block Diagram Detail -- Sequencer Module



**Figure 8**